Attorney's Docket No.:

7/204002/US3480D1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : H. Ohtani

Art Unit : 2815

Serial No.: 09/389,393 Filed

Examiner : B. William Baumeister

Title

: September 3, 1999

: SEMICONDUCTOR DEVICE HAVING INSULATED GATE ELECTION

BOX RCE

Commissioner for Patents Washington, D.C. 20231

AMENDMENT

Sir:

In response to the Official Action dated December 21, 2000, Paper No. 6, in the above-referenced application, please amend the above-identified application as follows.

In the Claims:

Please cancel claims 5, 10, 24, and 28 without prejudice.

Please amend claims 1, 6, 11, 16, 21, and 15 as follows.

(Amended) A semiconductor device comprising:

a semiconductor layer comprising a source region, a drain

region, and a channel region formed on an insulating surface;

a gate insulating film formed on said semiconductor layer;

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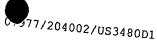
I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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Art Unit : 2815 Examiner : B. William Baumeister : SEMICONDUCTOR DEVICE HAVING INSULATED GATE ELECTROP

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TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims 30 Independent 24 6 6 \$108 \$0

Applicant hereby petitions under 37 C.F.R. \$1.136 for a 2 month extension of time.

TOTAL FEE DUE

\$390 \$498

A check for \$498 is attached.

Respectfully submitted,

Date: 5/21/01 JAMES T. HAGLER **REG. NO 40,631** coťt c. Fish & Richardson P.C. Reg. No. 32,030 TECHNOLOGY CENTER 2800

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